

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES**

In re Application of

Joel PAGE, et al.

Serial No.: 09/153,864

Filed: September 16, 1998

For: POWER ON RESET TECHNIQUES FOR AN INTEGRATED CIRCUIT CHIP



Group Art Unit: 2816

Examiner: J. Zweizig

*#16/1000
Appeal
JHM*

**NOTICE OF APPEAL
FROM THE PRIMARY EXAMINER
TO THE BOARD OF APPEALS**

Commissioner for Patents
Washington, DC 20231

Sir:

Applicants hereby appeal to the Board of Appeals from the decision dated September 29, 2000 of the Primary Examiner finally rejecting claims 1-13.

Appeal Fee: \$ 310.00

☐

Not required (fee paid in prior appeal in this application).

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Charge to Deposit Account No. 500417.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Gene Z. Robinson

Gene Z. Robinson

Registration No. 33,351

600 13th Street, N.W.
Washington, DC 20005-3096
(202) 756-8000 DLS/GZR:kap
Date: December 28, 2000
Facsimile: (202) 756-8087

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